/\* General uC definitions \*/

/\* Clock gate control \*/

**#define** CGC\_CLOCK\_DISABLED 0x00U

**#define** CGC\_CLOCK\_ENABLED 0x01U

/\* GPIO input / output \*/

**#define** GPIO\_INPUT 0x00U

**#define** GPIO\_OUTPUT 0x01U

/\* LCD definitions \*/

/\* LCD Register Selector

\* Used as register selector input

\* When (LCD\_RS = LCD\_RS\_HIGH) => DATA register is selected

\* When (LCD\_RS = LCD\_RS\_LOW) => INSTRUCTION register is selected

\*/

**#define** LCD\_PORT\_BASE\_PNT PORTC /\* peripheral port base pointer \*/

**#define** LCD\_GPIO\_BASE\_PNT PTC /\* peripheral gpio base pointer \*/

**#define** LCD\_RS\_PIN 8U /\* register selector \*/

**#define** LCD\_RS\_DIR (GPIO\_OUTPUT << LCD\_RS\_PIN)

**#define** LCD\_RS\_ALT kPortMuxAsGpio

**#define** LCD\_ENABLE\_PIN 9U /\* enable pin \*/

**#define** LCD\_ENABLE\_DIR (GPIO\_OUTPUT << LCD\_ENABLE\_PIN)

**#define** LCD\_ENABLE\_ALT kPortMuxAsGpio

**#define** LCD\_RS\_HIGH 1U

**#define** LCD\_RS\_DATA LCD\_RS\_HIGH

**#define** LCD\_RS\_LOW 0U

**#define** LCD\_RS\_CMD LCD\_RS\_LOW

**#define** LCD\_ENABLED 1U

**#define** LCD\_DISABLED 0U

**#define** LCD\_DATA\_DIR kGpioDigitalOutput /\* LCD data pins \*/

**#define** LCD\_DATA\_ALT kPortMuxAsGpio

**#define** LCD\_DATA\_DB0\_PIN 0u

**#define** LCD\_DATA\_DB1\_PIN 1u

**#define** LCD\_DATA\_DB2\_PIN 2u

**#define** LCD\_DATA\_DB3\_PIN 3U

**#define** LCD\_DATA\_DB4\_PIN 4U

**#define** LCD\_DATA\_DB5\_PIN 5U

**#define** LCD\_DATA\_DB6\_PIN 6U

**#define** LCD\_DATA\_DB7\_PIN 7U

**#define** LCD\_DATA\_DB0\_DIR (GPIO\_OUTPUT << LCD\_DATA\_DB0\_PIN)

**#define** LCD\_DATA\_DB1\_DIR (GPIO\_OUTPUT << LCD\_DATA\_DB1\_PIN)

**#define** LCD\_DATA\_DB2\_DIR (GPIO\_OUTPUT << LCD\_DATA\_DB2\_PIN)

**#define** LCD\_DATA\_DB3\_DIR (GPIO\_OUTPUT << LCD\_DATA\_DB3\_PIN)

**#define** LCD\_DATA\_DB4\_DIR (GPIO\_OUTPUT << LCD\_DATA\_DB4\_PIN)

**#define** LCD\_DATA\_DB5\_DIR (GPIO\_OUTPUT << LCD\_DATA\_DB5\_PIN)

**#define** LCD\_DATA\_DB6\_DIR (GPIO\_OUTPUT << LCD\_DATA\_DB6\_PIN)

**#define** LCD\_DATA\_DB7\_DIR (GPIO\_OUTPUT << LCD\_DATA\_DB7\_PIN)

/\* END OF LCD definitions \*/